MODELSIM/QUESTA CORE: HDL SIMULATION

DESCRIPTION

ModelSim /Questa Core: HDL Simulation teaches users new to using ModelSim or Questa SIM for HDL simulation how to effectively use ModelSim/Questa Core to verify VHDL, Verilog, SystemVerilog, and mixed HDL designs.

You will learn how ModelSim/Questa Core supports HDL behavioral simulations, and some basic concepts in the digital design flow. Also, you will receive an introduction on how to invoke the Visualizer debug environment to debug the simulation results from Questa. Hands-on lab exercises will reinforce lecture and discussion topics and provide you with extensive tool usage experience under the guidance of our industry expert instructors.

AGENDA

- Invoke the ModelSim/Questa Core program
- Prepare VHDL and Verilog data for use by ModelSim/Questa Core
- Create and use design libraries
- Use ModelSim/Questa Core commands to run a simulation
- Invoke Visualizer Debug Environment
- Create a simple simulation script
- Use ModelSim/Questa Core for batch simulations
- Use the ModelSim/Questa Core Graphical User Interface
- Create a ModelSim/Questa Core project
- Simulate VHDL or Verilog designs
- Simulate mixed VHDL/Verilog designs

PREREQUISITES

- Some VHDL or Verilog knowledge
- Some familiarity with digital design concepts

DURATION

1 Day

LANGUAGE

English or German

COSTS

Upon request*

www.trias-mikro.com

**including training materials*

TRAINER

Alexandru Vlad Velea
has an Electronics, Telecommunications and Information Technology University degree followed by MBA postgraduate degree.

From 2005 on he has been covering mostly the following Siemens products:
- HDL design, simulation and synthesis
- Wiring and harness design

He has a bright knowledge as consultant/ advisor/ technical support/ tools trainer. He is Wiring Harness consultant/ advisor for the Mentor Graphics / Siemens tools since 2011 and Digital IC flow (design/ simulation/ synthesis) consultant/ advisor for the Mentor Graphics/ Siemens tools starting 2005.

TRIAS is an Expert Partner of Siemens Digital Industries Software. Siemens Digital Industries Software awards the status "Expert" to sales partners who have in-depth expert knowledge in a product area or industry and have proven this repeatedly in reference projects.

Alexandru Vlad Velea is certified by Siemens for the products Capital | Capital Essentials (formerly VeSys®) for the automotive and aerospace (Aero) markets and continuously undergoes a mandatory certification program to verify and expand his competencies.

Moerser Landstraße 408
D-47802 Krefeld

+49 [0] 2151.95 301-0

+49 [0] 2151.95 301-15

info@trias-mikro.com

www.trias-mikro.com

ADDITIONAL COURSE

- ModelSim/ Questa Core: Advanced Topics

Subject to change