

MODELSIM/QUESTA CORE: ADVANCED TOPICS

DESCRIPTION

ModelSim/Quarta Core: Advanced Topics teaches you to capitalize on the extensive capabilities of ModelSim/Quarta Core to effectively and efficiently analyze and debug digital HDL designs. Using various ModelSim/Quarta Core features and techniques, you will learn how to produce higher performance test benches, more reliable device-under-test models, and greater confidence in simulation thoroughness and completeness.

AGENDA

- ▶ Use advanced debugging concepts and methods
- ▶ Address advanced design topics and issues
- ▶ Take advantage of advanced cross-window capabilities supporting debugging
- ▶ Manipulate designs and the ModelSim environment using Tcl/Tk
- ▶ Customize design monitors and comparators using Tcl/Tk
- ▶ Determine design "Code Coverage" for verification scenarios
- ▶ Use ModelSim in debugging and performance modes
- ▶ Use the profiler to find bottlenecks in your code
- ▶ Use "Virtual Objects" to explore designs under test
- ▶ Perform advanced design probing with "Signal Spy"
- ▶ Create and compare multiple datasets
- ▶ Use advanced waveform comparison features
- ▶ Visualize and debug Finite State Machines with the FSM Viewer
- ▶ Use ModelSim for simulating VHDL, Verilog, and SystemVerilog designs
- ▶ Analyze and improve design and end product performance from high-level abstract design description through gate-level implementations
- ▶ Tracing the cause of any signal event or all possible drivers of a signal
- ▶ Debug multiple types of specific design errors

PREREQUISITES

- ▶ VHDL or Verilog knowledge
- ▶ Beginner skills in ModelSim/Quarta Core or take the ModelSim/Quarta Core: HDL Simulation training class in advance

DURATION

2 Days

LANGUAGE

English or German

COSTS

Upon request*

www.trias-micro.com

**including training materials*



TRAINER

Hans-Jürgen Schwender


has a masters degree in electrical engineering. From 1991 until the end of 2001, he worked as an ASIC design engineer at Philips Kommunikationsindustrie and Lucent Technologies in Nuremberg and at Infineon Technologies in San Jose, CA, USA. He worked on the creation of specifications, the implementation in VHDL, verification on module and chip level as well as programming of ASIC Driver Software in C.

Mr. Schwender has been working at TRIAS mikroelektronik GmbH since 2002 and, as the technical manager covers a large part of Mentor's products - with a focus on HDL design, verification and cable harness design products.

ADDITIONAL COURSE

▶ **ModelSim/ Questa Core: HDL Simulation**

TRIAS 
microelectronics SRL

 Aleea Nicolina nr. 17
700221 Iasi | Romania

 +40 [0] 332.440 149

 +40 [0] 332.818 998

 info@trias-micro.com

 www.trias-micro.com