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FPGA VERIFICATION DAY 2019 - Register now

The registration deadline for FPGA Verification Day 2019 in Berlin is nearly here. If you have not registered yet, you should do so now - you only have until 12th September.

Our international experts Nigel Woolaway, Espen Tallaksen, Frederic Leens, Stefan Bauer and Hans-Jürgen Schwender are looking forward to an interesting day, providing you with information on the variety of methods and standards related to Functional Safety.

Participants who are not active in the field of functional safety will not miss out either, as the presentations will also contain generally interesting information for FPGA designers.

As you know, participation is free of charge and you have the chance to win JBL Bluetooth wireless headphones in the raffle at the end of the day - register now.

FPGA Verification Day
26th September 2019
Select Hotel Berlin Spiegelturm

Registration Deadline: 12th September 2019

Info & Registration

You may also want to find out about the variety of training courses we offer on FPGA design and validation.
Want to know more?
You can contact us by phone on +49 (0)2151/95301-0 or email us: education@trias-mikro.de.

Best regards
Team TRIAS

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